Product Specification
产品规格书

产品名称 ：2.0寸480*360点阵彩屏模组

规格型号 ：HD20034C40-H

部门确认：

研发	工程	品管	审核

印　　章：

日　　期：

客户回签：

采购	工程	品管	确认

印　章：

日　期：

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REVISION HISTORY

Rev	Description	Page	Date
1.0	Initial Release	All	2023-6-8

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1. GENERAL DESCRIPTION

1.1 DESCRIPTION

HD20034C40-H is a transmissive type color active matrix TFT (Thin Film Transistor) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT-LCD module (TFT-LCD panel, driver IC and FPC), a back-light unit and. The resolution of 2.0" contains 480 RGB X360 pixels and can display up to 262k colors.

1.2 GENERAL INFORMATION

Items	Specification	Unit	Note
Drive element	a-Si TFT	-	-
LCM outline size	46 (H) x 41 (V)	mm	
Active area	40.8 (H) x 30.6 (V)	mm	-
Number of pixels	480(H)X360(V)	pixels	-
Pixel arrangement	RGB stripe	-	-
Pixel Pitch	0.08505X0.08505	Mm	-
Display color	16.7M	color	-
Viewing direction	ALL o'clock	-	-
Controller / Driver	ST7701S	-	-
Data interface	MIPI/RGB16bit 兼容	-	
Backlight	6 White LEDs In Parallels	-	
Weight	TBD	g	

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2. ABSOLUTE MAXIMUM RATING

(Ta=25±2°C, Vss=GND=0V)

Characteristics	Symbol	Min.	Typ	Max.	Unit	Notes
Supply Voltage	IOVCC	-0.3	-	3.6	V	
	VCI	-0.3	-	3.6	V	
TFT Gate On voltage	VGH	-0.3	-	30	V	
TFT Gate Off voltage	VGL	-0.3	-	30	V	
Backlight Forward Current	I_F	-		120	mA	
Operating Temperature	T_{OPR}	-20		+70	°C	(1), (3)
Storage Temperature	T_{STG}	-30		+80	°C	(2), (3)
Humidity	RH	-		90	%	Max. 60 °C

Notes:

(1) In case of below 0°C, the response time of liquid crystal (LC) becomes slower and the color of panel becomes darker than normal one. Level of retardation depends on temperature, because of the LC characteristics.

(2) If product is exposed to high temperatures for extended time, there is a possibility of the polarizer film damage which could degrade the optical characteristics.

(3) Permanent damage to the device may occur if maximum values are exceeded or reverse voltage is loaded.
Functional operation should be restricted to the conditions described under normal operating conditions.

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3. ELECTRICAL CHARACTERISTICS

3.1 LCM DC CHARACTERISTICS

(Ta=25±2°C)

Characteristics	Symbol	Min.	Typ.	Max.	Unit	Note
Power Supply Voltage 1	IOVCC	1.65	1.8	3.3	V	
Power Supply Voltage 2	VCI	2.5	2.8	3.6	V	
Power Supply Voltage 3	-	-	-	-	V	
Power Supply for MTP	VPP	-	-	-	V	
Current Consumption	I_{DD}	-	TBD	-	mA	Normal mode
	$I_{DD-SLEEP}$		TBD		uA	Sleep mode
Input voltage “L” Level	V_{IL}	GND	-	0.3IOVCC	V	IOVCC=1.65~3.3
Input voltage “H” Level	V_{IH}	0.7IOVCC	-	IOVCC	V	
Output voltage “L” Level	V_{oL}	0	-	0.2IOVCC	V	I_{OL}=1mA
Output voltage “H” Level	V_{oH}	0.8IOVCC	-	IOVCC	V	I_{OH}=-1mA

3.2 BACK-LIGHT UNIT CHARACTERISTICS

The back-light system is an edge-lighting type with 4 white LEDs. The characteristics of the back-light are shown in the following tables.

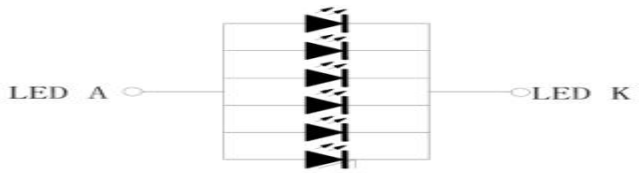
(Ta=25±2°C)

Characteristics	Symbol	Condition	Min.	Type	Max.	Unit	Notes
Forward Voltage	Vf	I_L=120mA	2.8	3.0	3.2	V	-
Forward current	I_L		-	120	-	mA	-
Luminance	Lv	I_L=120mA	450	500	--	cd/m^2	-
LED life time	-	I_L=120mA	20,000	25,000	--	Hr	Note 1

Note:

(1) The “LED life time” is defined as the module brightness decrease to 50% of original brightness at I_L=80mA. The LED life time could be decreased if operating I_L is larger than 80mA.

Bcklight circuit diagram shown in below:



背光电路图 (CIRCUIT DIAGRAM)
IF=120mA, VF=3.0V

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4. OPTICAL CHARACTERISTICS

The following items are measured under stable conditions. The optical characteristics should be measured in a dark room.
Measuring equipment: BM-5AS, BM-7, EZ-Contrast.

(Ta=25±2°C)

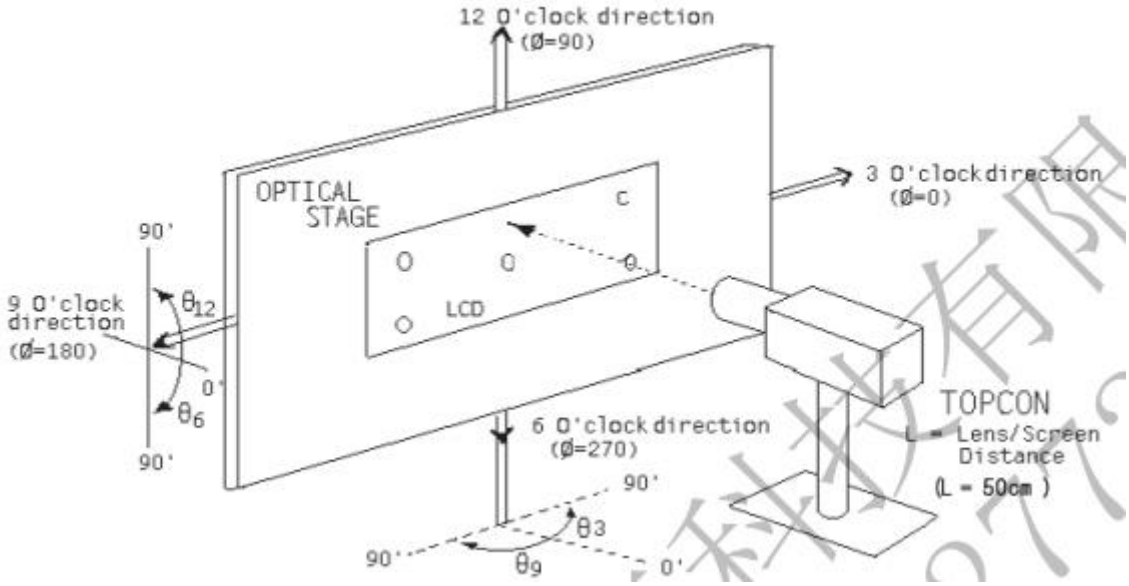
Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio (Center point)		C/R	-	800	850	-	-	BM-7 Note(2)
Luminance of white (Center point)		L_w	B/L on	15%	TBD	15%	cd/m^2	CA-210
Luminance uniformity		U_W	θ = 0. Normal viewing angle B/L On Note(1)	80	-	-	%	BM-7 Note(3)
Response Time		Tr + Tf		-	30	40	ms	BM-5AS Note(4)
Color Chromaticity (CIE 1931)	White	W_X		0.298	0.318	0.338	-	CA-210 Note(5)
		W_X		0.321	0.341	0.361		
	Red	R_X		0.618	0.638	0.658		
		R_Y		0.318	0.338	0.358		
	Green	G_X		0.276	0.296	0.316		
		G_y		0.555	0.575	0.595		
	Blue	B_X		0.131	0.137	0.157		
		B_Y		0.104	0.124	0.144		
Viewing Angle	Hor.	θ_T	C/R≥10	70	80	-	Deg	EZ Contrast Note(6)
		θ_B		70	80	-		
	Ver.	θ_L		70	80	-		
		θ_R		70	80	-		
Optima View Direction			ALL					Note(7)

* This condition will be changed by the evaluation circumstance. If product is exposed to high temperatures for extended time, there is a possibility of the polarizer film damage which could degrade the optical characteristics.

Notes:

(1) Test Equipment Setup: After stabilizing and leaving the panel alone at a given temperature for 30min, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room 30min after lighting the back-light. This should be measured in the center of screen.

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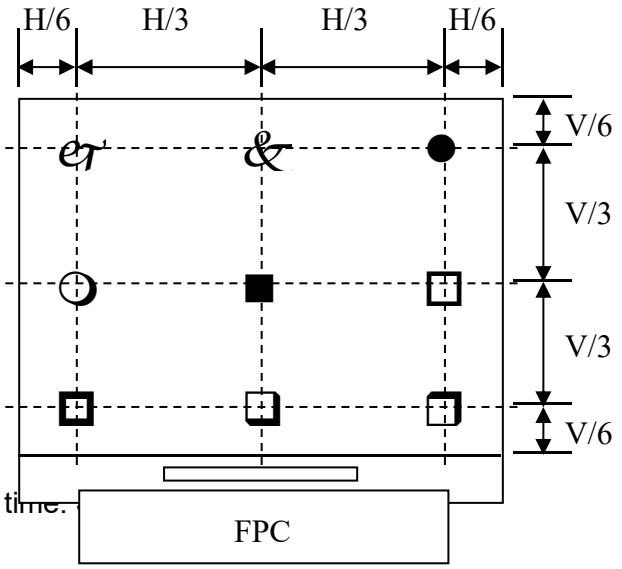


(2) Definition of Contrast Ratio (CR):

$$\text{Contrast Ratio (CR)} = \frac{\text{Luminance measured when LCD on the “white” state}}{\text{Luminance measured when LCD on the “black” state}}$$

(3) Definition of Luminance Uniformity: Active area is divided into 9 measuring areas (Shown in below), every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity} = \frac{\text{Min Luminance of white among 9-points}}{\text{Max Luminance of white among 9-points}} \times 100\%$$

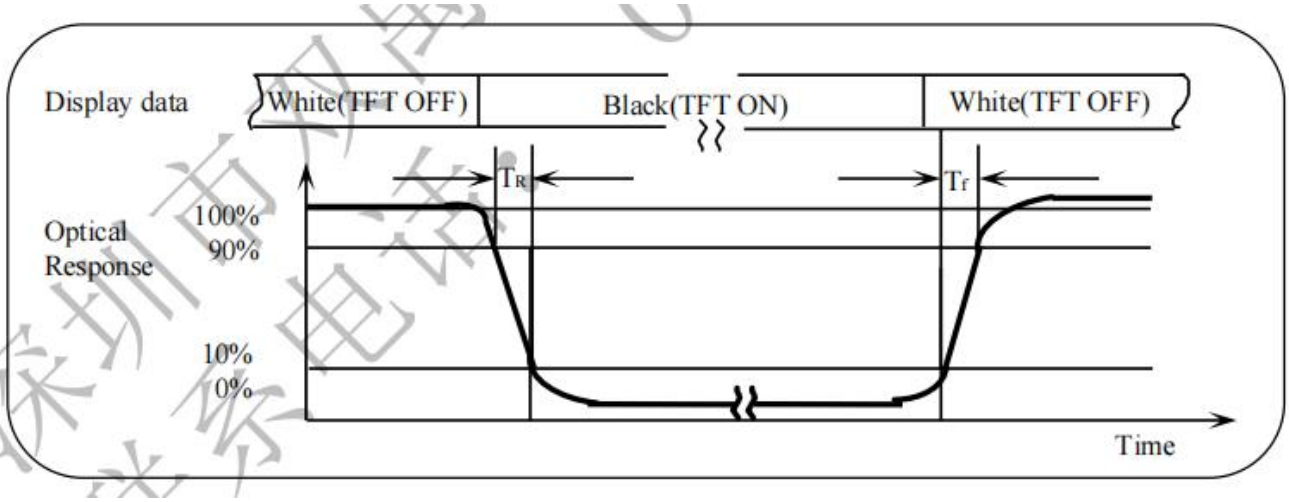


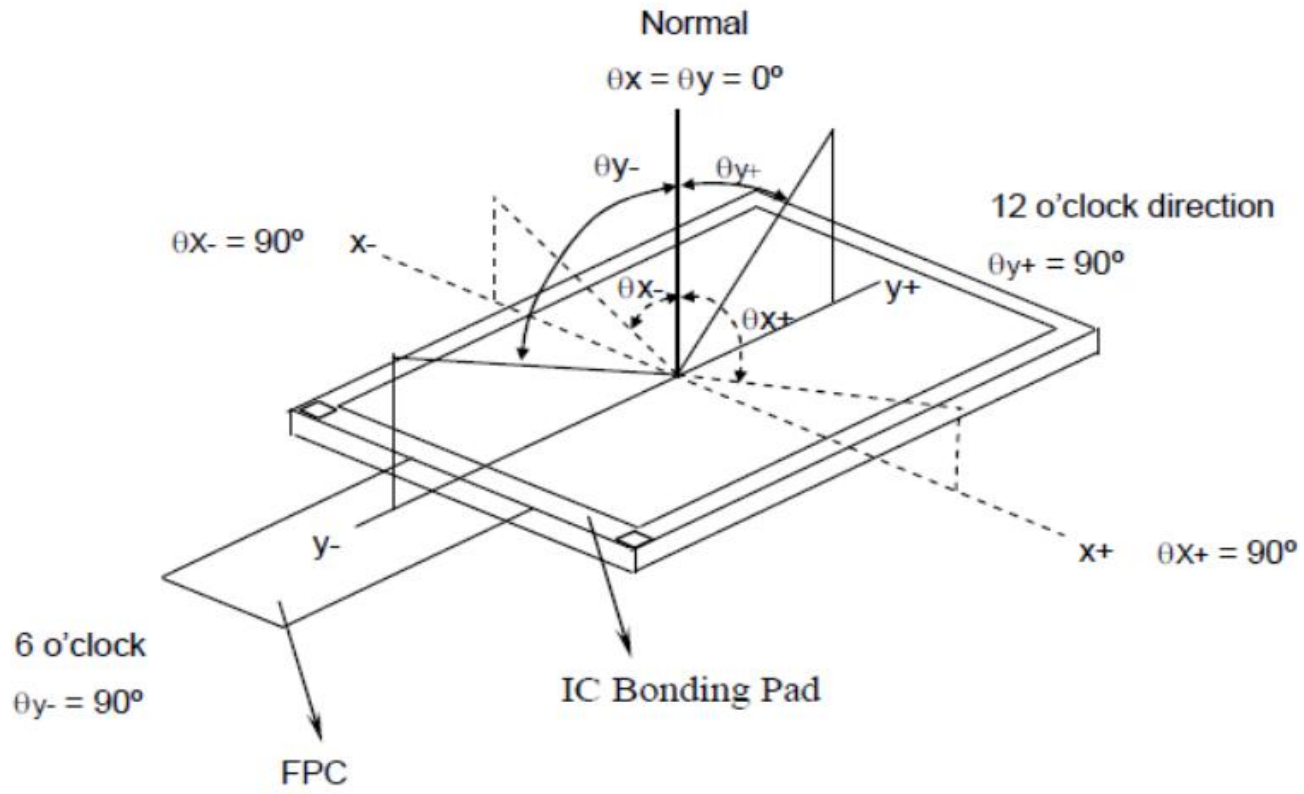
The spot locations for luminance measurement

(4) Definition of Response time:

(5) Definition of Viewing Angle: The viewing angle range that the CR≥10.

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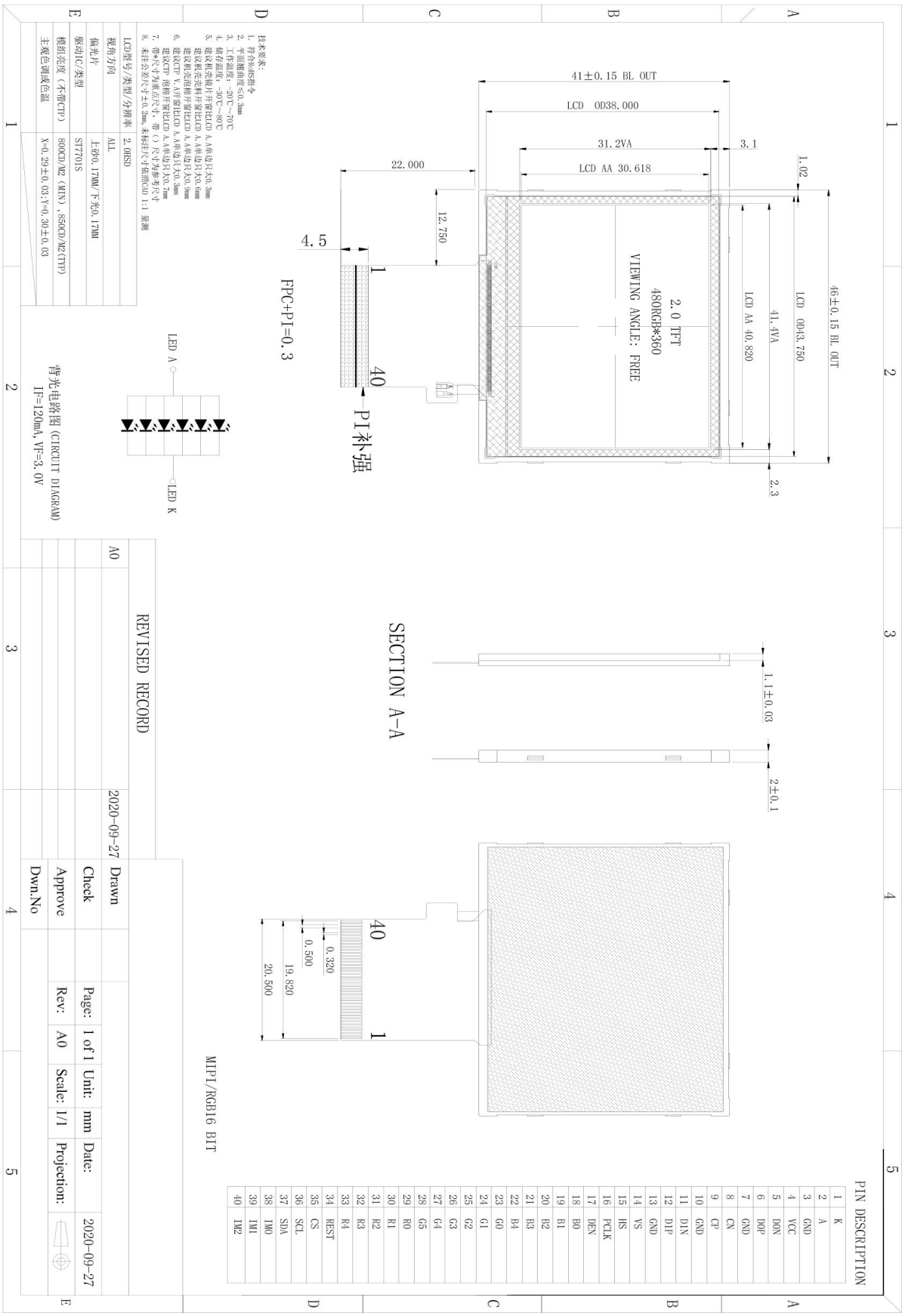


(6) Definition of Color Chromaticity (CIE 1931)
Color coordinate of white & red, green, blue at center point.

(7) The different Rubbing Direction will cause the different optima view direction.

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5.MODULE OUTLINE DIMENSION



5.

6.MODULE INTERFACE DESCRIPTION

Pin No.	Symbol	Description
1	K	Reset input pin
2	A	Back-light Anode
3	GND	Power Ground
4	VCC	Power supply for interface logic circuits(2.8V)
5	D0N	MIPI-DSI DATE signal input
6	D0P	MIPI-DSI DATE signal input
7	GND	Power Ground
8	CN	MIPI-DSI DATE signal input
9	CP	MIPI-DSI DATE signal input
10	GND	Power Ground
11	D1N	MIPI-DSI DATE signal input
12	D1P	MIPI-DSI DATE signal input
13	GND	Power Ground
14	VS	Frame synchronizing signal for RGB interface operation
15	HS	Line synchronizing signal for RGB interface operation
16	PCLK	Dot clock signal for RGB interface operation
17	DE	Data enable signal for RGB interface operation
18-22	B0-B4	parallel data bus for RGB Interface .
23-28	G0-G5	parallel data bus for RGB Interface .
29-33	R0-R4	parallel data bus for RGB Interface .
34	REST	Reset input pin
35	CS	A chip select signal
36	SCL	Serial clock input for SPI interface .
37	SDA	Serial data input / output bid irectional pin for SPI .
38	IM0	Select the MCU interface mode
39	IM1	Select the MCU interface mode

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40	IM2	Select the MCU interface mode

7.REFERENCE APPLICATION CIRCUIT

Please consult our technical department for detail information.

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8.TIMINGS FOR MIPI RGB Interface

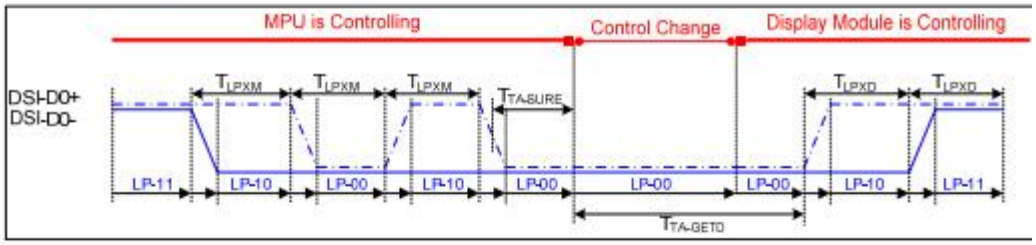


Figure 6 Bus Turnaround (BTA) from display module to MPU Timing

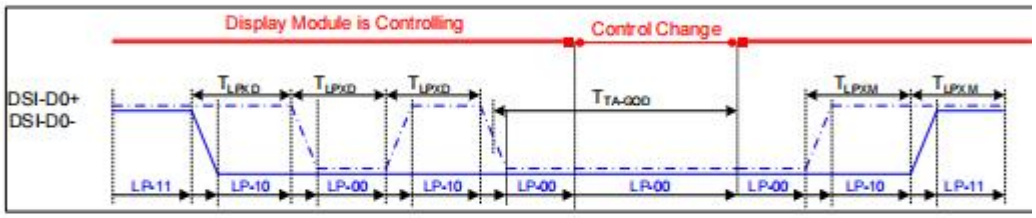


Figure 7 Bus Turnaround (BTA) from MPU to display module Timing

VDDI=1.8,VDD=2.8, AGND=DGND=0V, Ta=25 ℃

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
DSI-D0+/-	TLPXM	Length of LP-00,LP-01, LP-10 or LP-11 periods MPU→Display Module	50	75	ns	Input
DSI-D0+/-	TLPXD	Length of LP-00,LP-01, LP-10 or LP-11 periods MPU→Display Module	50	75	ns	Output
DSI-D0+/-	TTA-SURED	Time-out before the MPU start driving	T_{LPXD}	$2xT_{LPXD}$	ns	Output
DSI-D0+/-	TTA-GETD	Time to drive LP-00 by display module	$5xT_{LPXD}$		ns	Input
DSI-D0+/-	TTA-GOD	Time to drive LP-00 after turnaround request-MPU	$4xT_{LPXD}$		ns	Output

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7.5.1 Serial Interface Characteristics (3-line serial):

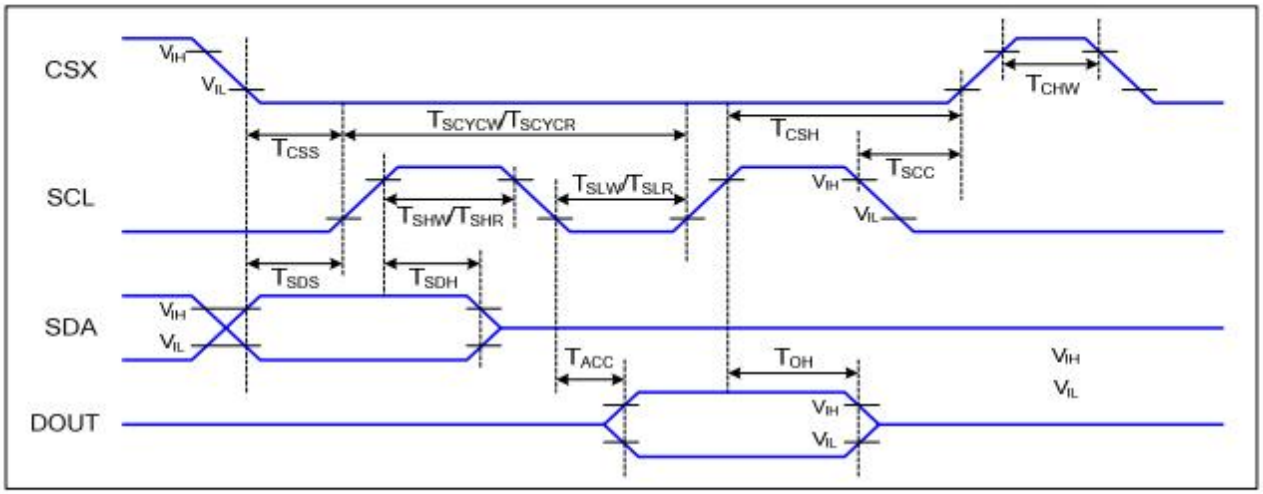


Figure 1 3-line serial Interface Timing Characteristics

VDDI=1.8,VDD=2.8, AGND=DGND=0V, Ta=25℃

Signal	Symbol	Parameter	Min	Max	Unit	Description
CSX	T_{CSS}	Chip select setup time (write)	15		ns	
	T_{CSH}	Chip select hold time (write)	15		ns	
	T_{CSS}	Chip select setup time (read)	60		ns	
	T_{SCC}	Chip select hold time (read)	60		ns	
	T_{CHW}	Chip select "H" pulse width	40		ns	
SCL	T_{SCYCW}	Serial clock cycle (Write)	66		ns	
	T_{SHW}	SCL "H" pulse width (Write)	15		ns	
	T_{SLW}	SCL "L" pulse width (Write)	15		ns	
	T_{SCYCR}	Serial clock cycle (Read)	150		ns	
	T_{SHR}	SCL "H" pulse width (Read)	60		ns	
	T_{SLR}	SCL "L" pulse width (Read)	60		ns	
SDA (DIN)	T_{SDS}	Data setup time	10		ns	
	T_{SDH}	Data hold time	10		ns	

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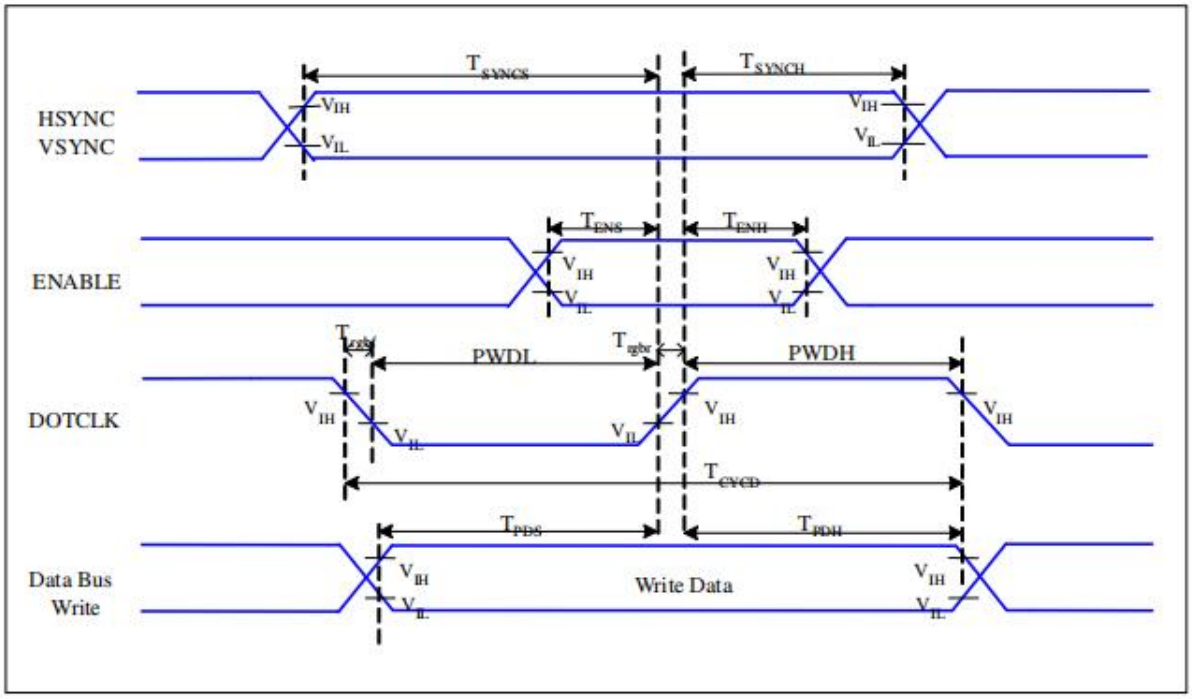


Figure 3 RGB Interface Timing Characteristics

VDDI=1.8,VDD=2.8, AGND=DGND=0V, Ta=25 ℃

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
HSYNC, VSYNC	T_{SYNCS}	VSYNC, HSYNC Setup Time	5	-	ns	
ENABLE	T_{ENS}	Enable Setup Time	5	-	ns	
	T_{ENH}	Enable Hold Time	5	-	ns	
DOTCLK	PWDH	DOTCLK High-level Pulse Width	15	-	ns	
	PWDL	DOTCLK Low-level Pulse Width	15	-	ns	
	T_{CYCD}	DOTCLK Cycle Time	33	-	ns	
	Trghr, Trghf	DOTCLK Rise/Fall time	-	15	ns	
DB	T_{PDS}	PD Data Setup Time	5	-	ns	
	T_{PDH}	PD Data Hold Time	5	-	ns	

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9.RELIABILITY TEST CONDITIONS

No.	Test Item	Test Condition	Notes
1	High Temperature Storage	+80°C / 240H	Inspection after 2~4h storage at room temperature, the sample shall be free from defects: 1. Air bubble in the LCD; 2. Seal leak; 3. Non-display; 4. Missing segments; 5.Glass crack; 6. The surface shall be free from damage. 7. The electrical characteristics requirements shall be satisfied.
2	Low Temperature Storage	-30°C / 240H	
3	High Temperature Operating	+70°C / 240H	
4	Low Temperature Operating	-20°C / 240H	
5	Temperature Cycle	Ta=-10°C~+25~+50°C,10 Cycle,per30min	
6	High Temperature /Humidity storage	60°C ,90%RH / 120H	
7	Vibration Test	Frequency: 10Hz~55Hz~10Hz Amplitude:1.5mm, 2 hours for each direction of X, Y, Z	
8	Packing Drop Test	Drop to the ground from 1m height, 1 corner, 3 edges, 6 surfaces.	
9	ESD test	Voltage:±8KV R: 330Ω C: 150pF Air discharge, Three or five times.	

Remarks:

(1) The test samples should be applied to only one test item.

(2) Sample size for each test item is 5~10pcs.

(3) For High Temperature/Humidity storage test, pure water (resistance>10MΩ) should be used.

(4) In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part.

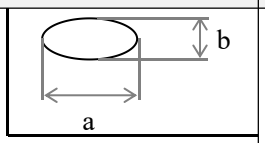
(5) Failure judgment criterion: basic specification, electrical characteristic, mechanical characteristic, optical characteristic.

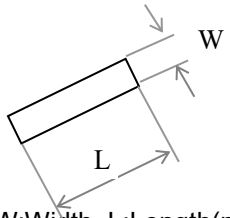
10.PACKING SPECIFICATION

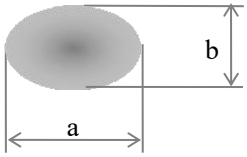
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11.INSPECTION CRITERION

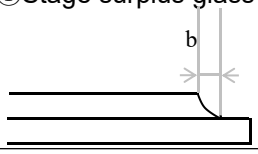
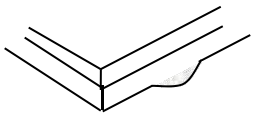
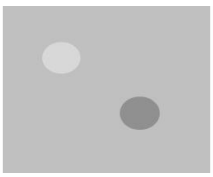
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<tr><th colspan="3" rowspan="3">Inspection item</th><th colspan="3">Judgement standard</th></tr>
<tr><th colspan="2" rowspan="2">Category</th><th colspan="2">Acceptable number</th></tr>
<tr><th>A zone</th><th>B zone</th></tr>
<tr><td rowspan="6">1</td><td rowspan="2">Black spot, White spot, Bright Spot, Pinhole Foreign Particle, Bubble and Particle Between polarizer and glass, scratch on polarizer</td><td rowspan="2">

Φ=(a+b)/2(mm)</td><td>A
B
C</td><td>Φ≦0.10
0.10<Φ≦0.20
Φ>0.2</td><td>Ignored
2
0</td><td>Ignored</td></tr>
<tr><td colspan="2">Total defective point(B,C)</td><td>3</td></tr>
<tr><td rowspan="5">Pixel point defect</td><td>Bright spot</td><td colspan="2">0.15<Φ≦0.20</td><td>N≤2</td><td rowspan="5">Ignored</td></tr>
<tr><td>Dark spot/ Black spot</td><td colspan="2">0.15<Φ≦0.20</td><td>N≤2</td></tr>
<tr><td>Attached to the two pixels are bright spots</td><td colspan="2">0.15<Φ≦0.20</td><td>N≤2</td></tr>
<tr><td>Even a two pixel is dark</td><td colspan="2">0.15<Φ≦0.20</td><td>N≤2</td></tr>
<tr><td>Pixel total number</td><td colspan="2">0.15<Φ≦0.20</td><td>N≤2</td></tr>
<tr><td colspan="5">Note1: the spot defect caused by foreign matter is judged according to the defect of the foreign body.
Note 2: when the light is not wired to show the type of defects.</td></tr>
<tr><td>2</td><td>Black line, White line, Bubble and Particle Between Polarizer and glass, Scratch on polarizer</td><td>

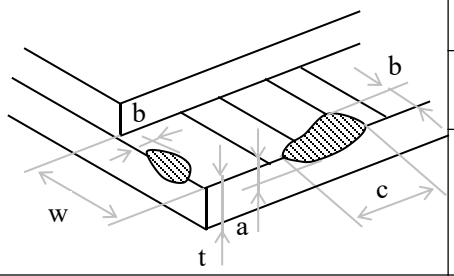
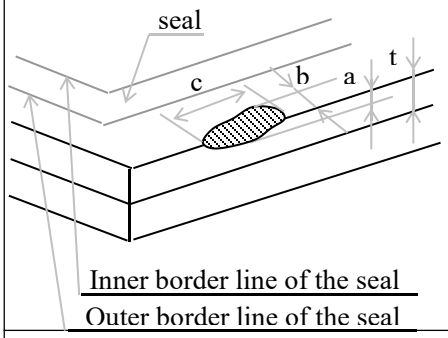
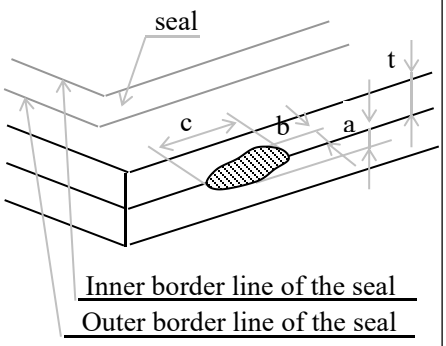
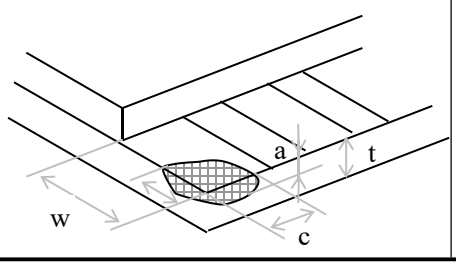
W:Width, L:Length(mm)</td><td>A
B
C</td><td>W≦0.03 L≦3.0
0.03<W≦0.05 L≦3.0
0.05<W</td><td>Ignored
2
0</td><td>Ignored</td></tr>
<tr><td colspan="2">Total defective point(B,C)</td><td>2</td></tr>
<tr><td rowspan="2">3</td><td rowspan="2">Contrast variation</td><td rowspan="2">

Φ=(a+b)/2(mm)</td><td>A
B
C</td><td>Φ≦0.1
0.1<Φ≦0.3
Φ>0.3</td><td>Ignored
2
0</td><td>Ignored</td></tr>
<tr><td colspan="2">Total defective point(B,C)</td><td>2</td><td></td></tr>
<tr><td>4</td><td>Bubble inside cell</td><td></td><td colspan="2">any size</td><td>none</td><td>none</td></tr>
<tr><td rowspan="3">5</td><td rowspan="3">Polarizer defect (if Polarizer is used)</td><td>Scratch and damage on polarizer, particle on polarizer or between polarizer and glass.</td><td colspan="4">Refer to item 1 and item 2.</td></tr>
<tr><td rowspan="2">Bubble, dent and convex</td><td>A
B
C</td><td>Φ≦0.3
0.3<Φ≦0.5
0.5<Φ</td><td>Ignored
2
0</td><td>Ignored</td></tr>
<tr><td colspan="2">Total defective point(B,C)</td><td>2</td></tr>
</table>

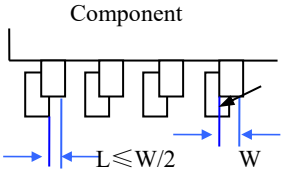
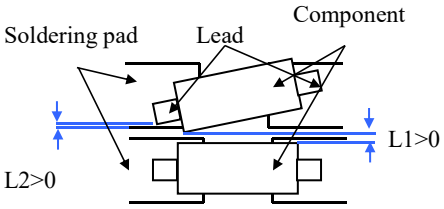
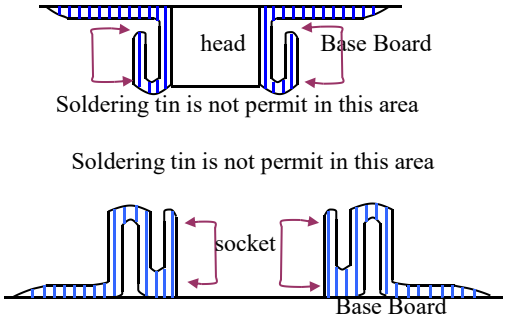
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Inspection item		Judgement standard		
		Category	Acceptable number	
			A zone	B zone
6	Surplus glass	①Stage surplus glass 	b≦0.3mm	
		②Surrounding surplus glass 	Should not influence outline dimension and assembling.	
7	MURA	①MURA	Naked eye examination: red, green, blue screen does not allow the appearance, black screen requires visual is not obvious, the specific reference limit samples. Note: the principle of closing the sample is to be installed on the whole machine and the end user will not find it in the normal usage scenario. Inspection basis: 6%ND (MURA mainly in the black screen and indoor light is relatively dark will be found, it is recommended to turn off the indoor lighting inspection.)	
		②Point Black / White / point(MURA)	1, under the black / gray screen check: D≦0.10mm Ignored; 0.10mm<D≦0.3mm, N≦2; D>0.3mm: Unqualified。 2, switch to the red, green, blue in which any one of the screen appears black or white or point to point white or point of failure. 	

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Inspection item			Judgment standard	
			Category(application: B zone)	
8	Glass defect crack	①The front of lead terminals 	A	If a≦t and b ≦1.0, c is not limited
			B	a≦t, 1≦b≦2mm, c≦3mm
			C	If glass crack cover alignment mark, b≦0.5mm.
			D	Crack at two sids of lead terminals should not cover patterns and alignment mark
		②Surrounding crack—non-contact side 	b < Inner borderline of the seal	
		③ Surrounding crack— contact side 	b < Outer borderline of the seal	
		④Corner 	A	a ≦ t, b ≦ 3.0, c ≦ 3.0
			*Glass crack should not cover patterns used for	

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Inspection item			Judgement standard
9	FPC defect	Component soldering: No cold soldering, short/open circuit, burr, tin ball. The flat encapsulation component position deviation must be less than 1/2 width of the pin (Pic.1)； The sheet component deviation: pin deviates from the pad and contact with the near components is not permitted（Pic.2）	
		lead defect: The lead lack must be less than 1/2of its width; The lead burr must be less than 1/2 of the seam; Impurities connect with the near leads is not permitted	
		Connector soldering: Soldering tin is at contact position of the plug and socket is not permitted No foundation is scald Serious cave distortion on plug and socket contact pin is not permitted	

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12.GENERAL PRECAUTIONS

1.1 HANDING

(1) When the module is assembled, it should be attached to the system firmly. Be careful not to twist and bent the module.

(2) Refrain from strong mechanical shock and / or any force to the module. In addition to damage, this may cause improper operation or damage to the module and back-light unit.

(3) Note that display modules are very fragile and could be easily damaged. Do not press or scratch the surface harder than a HB pencil lead.

(4) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, straining and discoloration may occur.

(5) If the display module surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, should be wiped by moisten cloth with isopropyl alcohol or ethyl alcohol solvents, DO NOT with water, ketone type materials (e.g. acetone), aromatic, toluene, ethyl acid or methyl chloride, and so on.

(6) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs or clothes, it must be washed away thoroughly with soap.

(7) Use finger-stalls with sort gloves in order to keep display clean during the incoming inspection and assembly process.

(8) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.

(9) Do not touch directly conductive parts such as the CMOS LSI pad and the interface terminals with bare hands, therefore operations should be grounded whenever he/she comes into contact with the modules.

(10) Do not exceed the absolute maximum rating value. (The supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on), otherwise the module may be damaged.

1.2 SOLDERING

(1) Use soldering irons with proper grounding and no leakage.

(2) For No RoHS Product: soldering temperature is 290~350°C, soldering time is 3~5s; for RoHS Product: soldering temperature is 340~370°C, soldering time is 3~5s.

(3) If soldering flux is used, be sure to remove any remaining flux after soldering (This does not apply in the case of a non-halogen type of flux).

1.3 STORAGE

(1) DO NOT leave the module in high temperature and high humidity for a long times, keep the temperature from 0°C to 35°C and relative humidity of less than 60%.

(2) It is highly recommended to store the module in a dark place. The Liquid crystal is deteriorated by ultraviolet, DO NOT leave it in direct sunlight and strong ultraviolet ray for many hours.

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