Product Specification
产品规格书

产品名称 ：4.3寸480*272点阵彩屏模组

规格型号 ：HD43017C40-H

部门确认：

研发	工程	品管	审核

印　　章：

日　　期：

客户回签：

采购	工程	品管	确认

印　章：

日　期：

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REVISION HISTORY

Rev	Description	Page	Date
1.0	Initial Release	All	2026-8-12

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1. GENERAL DESCRIPTION

1.1 DESCRIPTION

HD43017C40-H is a transmissive type color active matrix TFT (Thin Film Transistor) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT-LCD module (TFT-LCD panel, driver IC and FPC), a back-light unit and. The resolution of 4.3" contains 480 RGB X272 pixels and can display up to 16.7m colors.

1.2 GENERAL INFORMATION

Items	Specification	Unit	Note
Drive element	a-Si TFT	-	-
LCM outline size	105.40 (H) x67.07 (V)	mm	
Active area	95.04 (H) x 53.86(V)	mm	-
Number of pixels	480(H)X272(V)	pixels	-
Pixel arrangement	RGB stripe	-	-
Pixel Pitch	198(H) X 198(V)	mm	-
Display color	16.7M color	color	-
Viewing direction	ALL	-	-
Controller / Driver	NV3041A	-	-
Data interface	SPI/MCU	-	
Backlight	9 White LED	-	
Weight	TBD	g	

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2. ABSOLUTE MAXIMUM RATING

(Ta=25±2°C, Vss=GND=0V)

Characteristics	Symbol	Min.	Typ	Max.	Unit	Notes
Power Supply Voltage	VCI	-0.3	-	4.6	V	
Input Voltage	VDDI	-0.3	-	4.6	V	
Current of LED	ILED	0	-	20	mA	
Backlight Forward Current	I_F	-		180	mA	
Operating Temperature	T_{OPR}	-20		+70	°C	(1), (3)
Storage Temperature	T_{STG}	-30		+80	°C	(2), (3)
Humidity	RH	-		90	%	Max. 60 °C

Notes:

(1) In case of below 0°C, the response time of liquid crystal (LC) becomes slower and the color of panel becomes darker than normal one. Level of retardation depends on temperature, because of the LC characteristics.

(2) If product is exposed to high temperatures for extended time, there is a possibility of the polarizer film damage which could degrade the optical characteristics.

(3) Permanent damage to the device may occur if maximum values are exceeded or reverse voltage is loaded.
Functional operation should be restricted to the conditions described under normal operating conditions.

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3. ELECTRICAL CHARACTERISTICS

3.1 LCM DC CHARACTERISTICS

(Ta=25±2°C)

Characteristics	Symbol	Min.	Typ.	Max.	Unit	Note
Power Supply Voltage 1	IOVCC	1.6	-	3.6	V	
Power Supply Voltage 2	VCI	3	3.3	3.6	V	
Power Supply Voltage 3	-	-	-	-	V	
Power Supply for MTP	VPP	-	-	-	V	
Current Consumption	I_{DD}	-	TBD	-	mA	Normal mode
	$I_{DD-SLEEP}$		TBD		uA	Sleep mode
Input voltage “L” Level	V_{IL}	GND	-	0.3IOVCC	V	IOVCC=1.65~3.3
Input voltage “H” Level	V_{IH}	0.7IOVCC	-	IOVCC	V	
Output voltage “L” Level	V_{oL}	GND	-	DGND+0.4	V	I_{OL}=1mA
Output voltage “H” Level	V_{oH}	VDDI-0.4	-	IOVCC	V	I_{OH}=-1mA

3.2 BACK-LIGHT UNIT CHARACTERISTICS

The back-light system is an edge-lighting type with 9 white LEDs. The characteristics of the back-light are shown in the following tables.

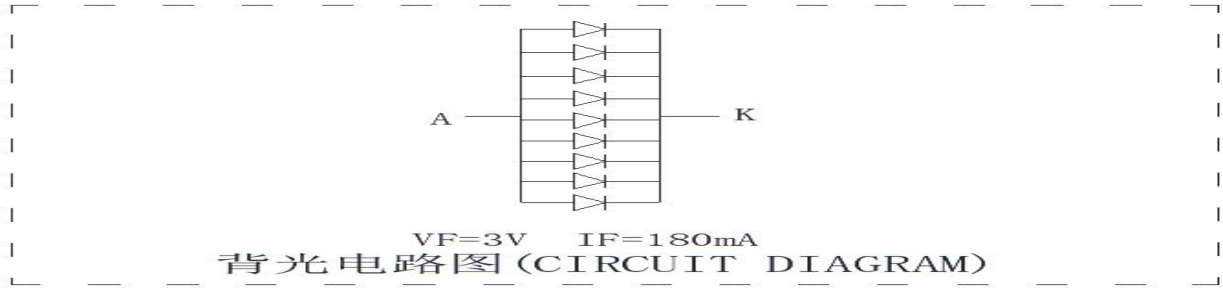
(Ta=25±2°C)

Characteristics	Symbol	Condition	Min.	Type	Max.	Unit	Notes
Forward Voltage	Vf	I_L=180mA	-	6	-	V	-
Forward current	I_L		-	180	-	mA	-
Luminance	Lv	I_L=180mA	--	300	--	cd/m^2	-
LED life time	-	I_L=180mA	20,000	25,000	--	Hr	Note 1

Note:

(1) The “LED life time” is defined as the module brightness decrease to 50% of original brightness at I_L=180mA. The LED life time could be decreased if operating I_L is larger than 180mA.

Bcklight circuit diagram shown in below:



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4. OPTICAL CHARACTERISTICS

The following items are measured under stable conditions. The optical characteristics should be measured in a dark room.
Measuring equipment: BM-5AS, BM-7, EZ-Contrast.

(Ta=25±2°C)

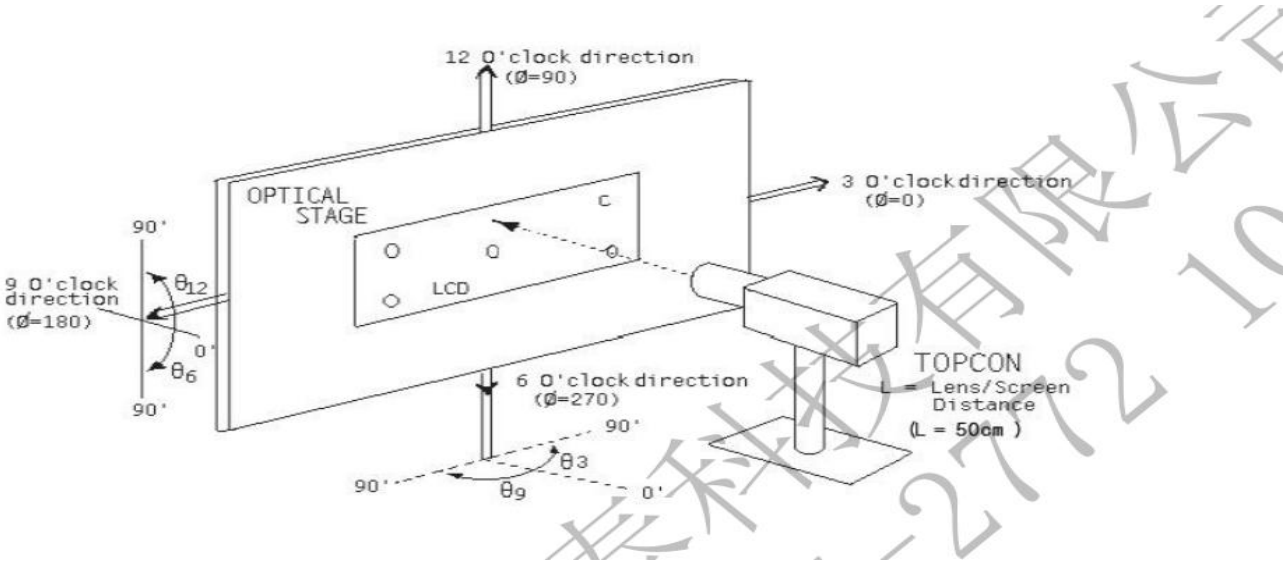
Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio (Center point)		C/R	-	800	1000	-	-	BM-7 Note(2)
Luminance of white (Center point)		L_w	B/L on	15%	TBD	15%	cd/m^2	CA-210
Luminance uniformity		U_W	θ = 0. Normal viewing angle B/L On Note(1)	80	-	-	%	BM-7 Note(3)
Response Time		Tr + Tf		-	30	35	ms	BM-5AS Note(4)
Color Chromaticity (CIE 1931)	White	W_X		0.275	0.305	0.335	-	CA-210 Note(5)
		W_X		0.306	0.336	0.366		
	Red	R_X		0.573	0.603	0.633		
		R_Y		0.287	0.307	0.337		
	Green	G_X		0.284	0.314	0.344		
		G_y		0.527	0.557	0.587		
	Blue	B_X		0.115	0.145	0.175		
		B_Y		0.123	0.153	0.183		
Viewing Angle	Hor.	θ_T	C/R≥10	80	85	-	Deg	EZ Contrast Note(6)
		θ_B		80	85	-		
	Ver.	θ_L		80	85	-		
		θ_R		80	85	-		
Optima View Direction			ALL					Note(7)

* This condition will be changed by the evaluation circumstance. If product is exposed to high temperatures for extended time, there is a possibility of the polarizer film damage which could degrade the optical characteristics.

Notes:

(1) Test Equipment Setup: After stabilizing and leaving the panel alone at a given temperature for 30min, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room 30min after lighting the back-light. This should be measured in the center of screen.

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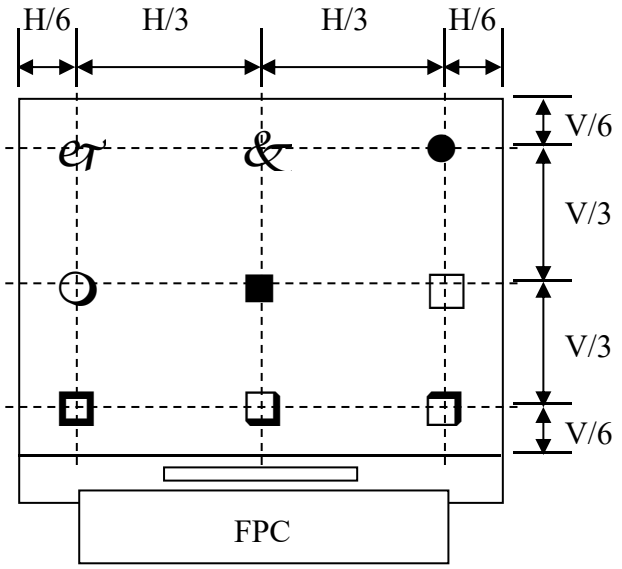


(2) Definition of Contrast Ratio (CR):

$$\text{Contrast Ratio (CR)} = \frac{\text{Luminance measured when LCD on the “white” state}}{\text{Luminance measured when LCD on the “black” state}}$$

(3) Definition of Luminance Uniformity: Active area is divided into 9 measuring areas (Shown in below), every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity} = \frac{\text{Min Luminance of white among 9-points}}{\text{Max Luminance of white among 9-points}} \times 100\%$$



The spot locations for luminance measurement

(4) Definition of Response time:

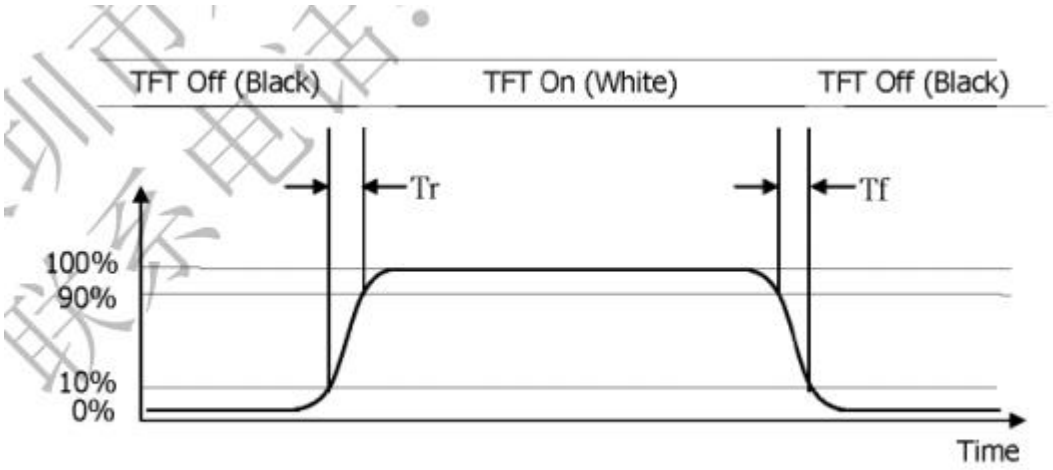
The output signals of photo detector are measured when the input signals are

Changed from “black” to “white” (rising time) and from time “white” to “black” (falling time),

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Respectively.

The response time is defined as the time interval between the 10% and 90% of Amplitudes. Refer to figure as below.

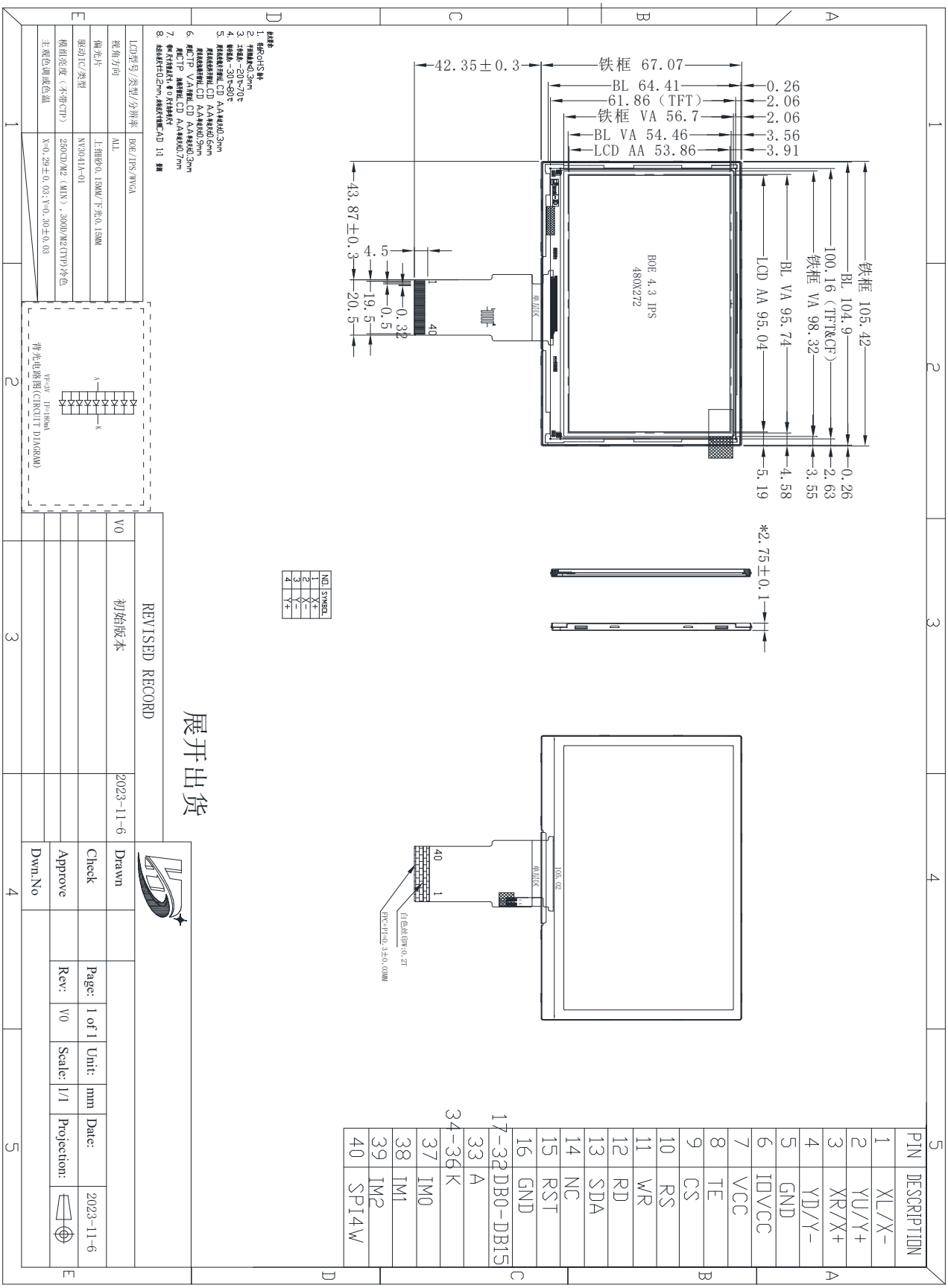


(5) Definition of Color Chromaticity (CIE 1931)
Color coordinate of white & red, green, blue at center point.

(6) The different Rubbing Direction will cause the different optima view direction.

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5.MODULE OUTLINE DIMENSION



PIN	DESCRIPTION
1	XL/X-
2	YU/Y+
3	XR/X+
4	YD/Y-
5	GND
6	IOVCC
7	VCC
8	TE
9	CS
10	RS
11	WR
12	RD
13	SDA
14	NC
15	RST
16	GND
17-32	DB0-DB15
33	A
34-36	K
37	IM0
38	IM1
39	IM2
40	SPI4W

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6.MODULE INTERFACE DESCRIPTION

Pin No.	Symbol	Description
1	XL/X-	触摸屏 X 轴低电压/负向输入端
2	YU/Y+	触摸屏 Y 轴高电压/正向输入端
3	XR/X+	触摸屏 X 轴高电压/正向输入端
4	YD/Y-	触摸屏 Y 轴低电压/负向输入端
5	GND	Power Ground
6	IOVCC	Power supply for interface logic circuits(1.8V-2.8V)
7	VCC	Power supply for interface logic circuits(2.8V-3.2V)
8	TE	Tearing effect output pin. Output signal low wait send image data.
9	CS	Chip select input pin
10	RS	Data or Command flag DCX=“H is data DCX=“” is command Internal pull high.
11	WR	Clock input, internal pull high.
12	RD	MCU interface read enable input, internal pull high.
13	SDA	Serial communication data input and output, internal pull low.
14	NC	NC
15	REST	Reset input pin
16	GND	Power Ground
17-32	DB0-DB15	16-bit parallel bi-directional data bus for MCU system
33	LEDA	Back-light Anode
34-36	LEDK	Back-light Cathode
37	IM0	Select the MCU interface mode
38	IM1	Select the MCU interface mode
39	IM2	Select the MCU interface mode
40	SPI4W	SPI4W=“1”, 4 wire SPI; SPI4W=“0”, 3wire SPI.

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IM2	IM1	IM0	Interface	Read Back Data Bus Selection
0	0	0	8080 series 8bit	DG3-2, DB7-2
			8080 series 9bit	DG4-2, DB7-2
			8080 series 16bit	DR5-2, DG7-2, DB7-2
0	0	1	3/4-wire Std SPI	SDA: In/Out
0	1	0	Dual SPI	SDA:In/Out DCX:In
0	1	1	Quad SPI	SDA:In/Out DCX:In DB[2]:In DB[3]:In
1	0	0	Parallel 18-bit RGB	DR7-2, DG7-2, DB7-2
			Serial 6-bit RGB	DG7-2(Default) Use register E1H bit[1:0] to select from DR7-2, DG7-2 or DB7-2

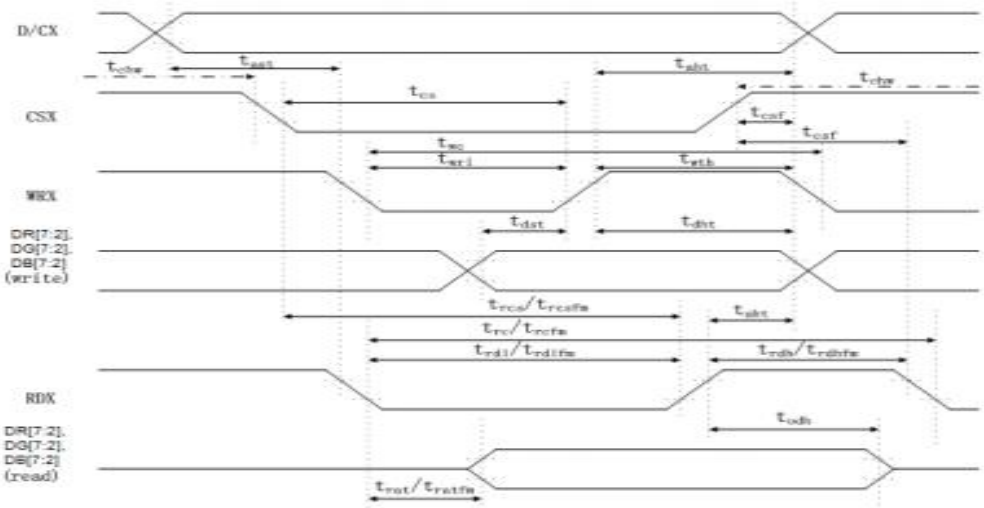
7.REFERENCE APPLICATION CIRCUIT

Please consult our technical department for detail information.

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8. AC Characteristics

8.3.1. Parallel MCU 16/9/8-bit BUS



Note: Logic high and low levels are specified as 30% and 70% of IOVCC for Input signals.

Table 8.3.1 AC characteristics of parallel MCU in asynchronous mode

Signal	Symbol	Parameter	MIN	MAX	UNIT	Description
D/CX	T_{AST}	Address Setup Time	0		ns	
	T_{AHT}	Address Hold Time (W/R)	10		ns	
CSX	T_{CHW}	"S" "H" Pulse Width	25		ns	
	T_{CS}	Chip Select Setup Time(W)	10		ns	
	T_{RCS}	Chip Select Setup Time (Read ID)	45		ns	
	T_{RCSFM}	Chip Select Setup Time (Read FM)	355		ns	
	T_{CSF}	Chip Select Wait Time (W/R)	10		ns	

Signal	Symbol	Parameter	MIN	MAX	UNIT	Description
WRX	T_{WC}	Write Cycle	50		ns	MCU 16 Bit Format (5-6-5): Twc> 100ns (see "6.4.8.") MCU 16 Bit Format (6-6-6): Twc>66ns (see "6.4.9." Figure 6.4.9.4) Other Format Twc>50ns
	T_{WRH}	Control Pulse H Duration	T_{WC} /2		ns	
	T_{WRL}	Control Pulse L Duration	T_{WC} /2		ns	
RDX	T_{RC}	Read Cycle(ID)	160		ns	When Read ID
	T_{RDH}	Control Pulse H Duration(ID)	T_{RC} /2		ns	
	T_{RDL}	Control Pulse L Duration(ID)	T_{RC} /2		ns	
RDX	T_{RCFM}	Read Cycle(FM)	450		ns	When Read From Frame Memory
	T_{RDHFM}	Control Pulse H Duration(FM)	T_{RCFM} /2		ns	
	T_{RDLFM}	Control Pulse L Duration(FM)	T_{RCFM} /2		ns	
DR[7:2], DG[7:2], DB[7:2]	T_{DST}	Data Setup Time	10		ns	CLmax=30pF Clmin=8pF
	T_{DHT}	Data Hold Time	10		ns	
	T_{RAT}	Read Access Time(ID)		40	ns	
	T_{RATFM}	Read Access Time(FM)		340	ns	
	T_{ODH}	Output Disable Time	20	80	ns	

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8.3.2. Display Serial Interface (SPI/Dual-SPI/Quad-SPI)

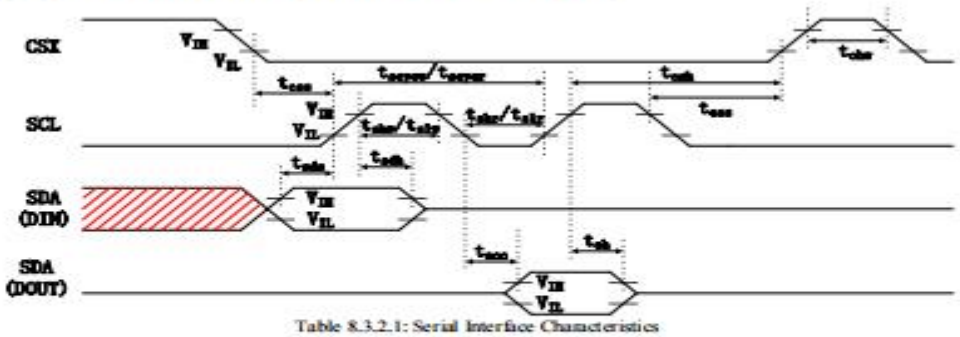


Table 8.3.2.1: Serial Interface Characteristics

Signal	Symbol	Parameter	MIN	MAX	UNIT	Description
CSX	T_{CSS}	Chip Select Setup Time	10		ns	
	T_{CSH}	Chip Select Hold Time	30		ns	
	T_{CHW}	Chip Select "H" Pulse Width	30		ns	
SCL	T_{SCYCW}	Serial Clock Cycle(Write)	12.5		ns	QSPI 4 lane format (5-6-5): T_{SCYCW} >25ns (see "6.4.12.") QSPI 4 lane format (6-6-6): T_{SCYCW} >16ns(see "6.4.13") Other Format T_{SCYCW} >12.5ns
	T_{SHW}	S"L" "H" Pulse Width(Write)	T_{SCYCW} /2		ns	
	T_{SLW}	S"L" "L" Pulse Width(Write)	T_{SCYCW} /2		ns	
	T_{SCYCR}	Serial Clock Cycle(Read)	150		ns	
	T_{SHR}	S"L" "H" Pulse Width(Read)	T_{SCYCR} /2		ns	
	T_{SLR}	S"L" "L" Pulse Width(Read)	T_{SCYCR} /2		ns	
SDA(DIN) /(DOUT)	T_{SDS}	Data Setup Time	5		ns	
	T_{SDH}	Data Hold Time	5		ns	
	T_{ACC}	Access Time	5	50	ns	CLmax=30pF CLmin=8pF
	T_{OH}	Output Disable Time	10		ns	

Table 8.3.2.2: 4 wire Serial Interface Characteristics

Signal	Symbol	Parameter	MIN	MAX	UNIT	Description
CSX	T_{CSS}	Chip Select Setup Time	10		ns	
	T_{CSH}	Chip Select Hold Time	30		ns	
	T_{CHW}	Chip Select "H" Pulse Width	30		ns	
SCL	T_{SCYCW}	Serial Clock Cycle(Write)	12.5		ns	
	T_{SHW}	S "L" "H" Pulse Width(Write)	T_{SCYCW} /2		ns	
	T_{SLW}	S "L" "L" Pulse Width(Write)	T_{SCYCW} /2		ns	
	T_{SCYCR}	Serial Clock Cycle(Read)	150		ns	
	T_{SHR}	S "L" "H" " Pulse Width(Read)	T_{SCYCR} /2		ns	
	T_{SLR}	S"L" "L" Pulse Width(Read)	T_{SCYCR} /2		ns	
D/CX	T_{DCS}	D/CX Setup Time	5		ns	
	T_{DCH}	D/CX Hold Time	5		ns	
SDA(DIN) (DOUT)	T_{SDS}	Data Setup Time	5		ns	
	T_{SDH}	Data Hold Time	5		ns	
	T_{ACC}	Access Time	5	50	ns	CLmax=30pF CLmin=8pF
	T_{OH}	Output Disable Time	10		ns	

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9.RELIABILITY TEST CONDITIONS

No.	Test Item	Test Condition	Notes
1	High Temperature Storage	+80°C / 240H	Inspection after 2~4h storage at room temperature, the sample shall be free from defects: 1. Air bubble in the LCD; 2. Seal leak; 3. Non-display; 4. Missing segments; 5.Glass crack; 6. The surface shall be free from damage. 7. The electrical characteristics requirements shall be satisfied.
2	Low Temperature Storage	-30°C / 240H	
3	High Temperature Operating	+70°C / 240H	
4	Low Temperature Operating	-20°C / 240H	
5	Temperature Cycle	Ta=-10°C~+25~+50°C,10 Cycle,per30min	
6	High Temperature /Humidity storage	60°C ,90%RH / 240H	
7	ESD test	Open Cell , Air mode , + 2 KV	

Remarks:

(1) The test samples should be applied to only one test item.

(2) Sample size for each test item is 5~10pcs.

(3) For High Temperature/Humidity storage test, pure water (resistance>10MΩ) should be used.

(4) In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part.

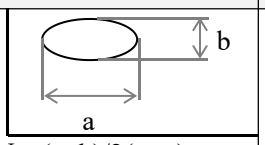
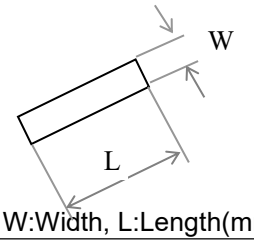
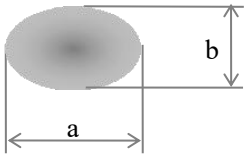
(5) Failure judgment criterion: basic specification, electrical characteristic, mechanical characteristic, optical characteristic.

10.PACKING SPECIFICATION

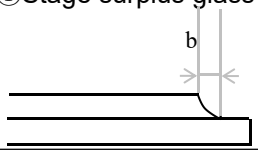
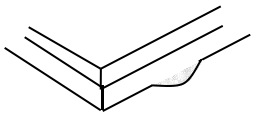
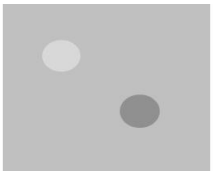
TBD

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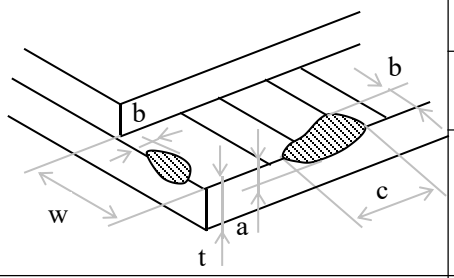
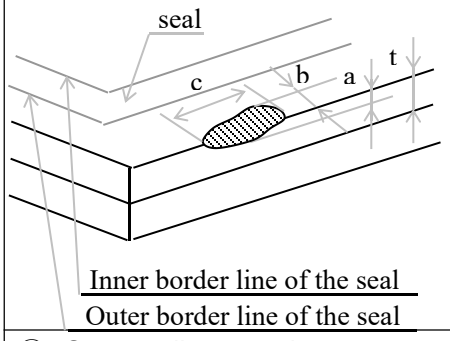
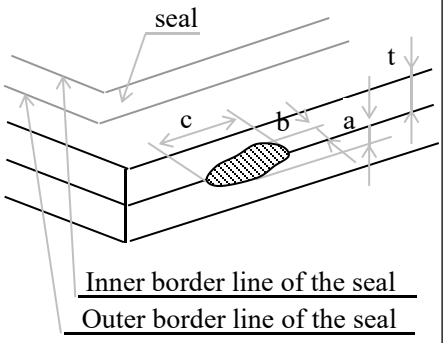
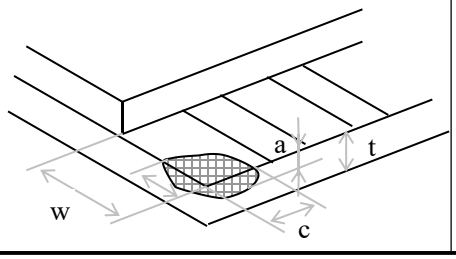
11.INSPECTION CRITERION

<table>
<tr><th colspan="3" rowspan="3">Inspection item</th><th colspan="3">Judgement standard</th></tr>
<tr><th colspan="2" rowspan="2">Category</th><th colspan="2">Acceptable number</th></tr>
<tr><th>A zone</th><th>B zone</th></tr>
<tr><td rowspan="7">1</td><td rowspan="2">Black spot, White spot, Bright Spot, Pinhole Foreign Particle, Bubble and Particle Between polarizer and glass, scratch on polarizer</td><td rowspan="2">
Φ=(a+b)/2(mm)</td><td>A
B
C</td><td>Φ≦0.10
0.10<Φ≦0.20
Φ>0.2</td><td>Ignored
2
0</td><td rowspan="2">Ignored</td></tr>
<tr><td colspan="2">Total defective point(B,C)</td><td>3</td></tr>
<tr><td rowspan="5">Pixel point defect</td><td>Bright spot</td><td colspan="2">0.15<Φ≦0.20</td><td>N≤2</td><td rowspan="6">Ignored</td></tr>
<tr><td>Dark spot/ Black spot</td><td colspan="2">0.15<Φ≦0.20</td><td>N≤2</td></tr>
<tr><td>Attached to the two pixels are bright spots</td><td colspan="2">0.15<Φ≦0.20</td><td>N≤2</td></tr>
<tr><td>Even a two pixel is dark</td><td colspan="2">0.15<Φ≦0.20</td><td>N≤2</td></tr>
<tr><td>Pixel total number</td><td colspan="2">0.15<Φ≦0.20</td><td>N≤2</td></tr>
<tr><td colspan="5">Note1: the spot defect caused by foreign matter is judged according to the defect of the foreign body.
Note 2: when the light is not wired to show the type of defects.</td></tr>
<tr><td>2</td><td>Black line, White line, Bubble and Particle Between Polarizer and glass, Scratch on polarizer</td><td>
W:Width, L:Length(mm)</td><td>A
B
C</td><td>W≦0.03 L≦3.0
0.03<W≦0.05 L≦3.0
0.05<W</td><td>Ignored
2
0</td><td>Ignored</td></tr>
<tr><td colspan="3">Total defective point(B,C)</td><td>2</td></tr>
<tr><td>3</td><td>Contrast variation</td><td>
Φ=(a+b)/2(mm)</td><td>A
B
C</td><td>Φ≦0.1
0.1<Φ≦0.3
Φ>0.3</td><td>Ignored
2
0</td><td>Ignored</td></tr>
<tr><td colspan="3">Total defective point(B,C)</td><td>2</td></tr>
<tr><td>4</td><td>Bubble inside cell</td><td></td><td colspan="2">any size</td><td>none</td><td>none</td></tr>
<tr><td rowspan="3">5</td><td rowspan="3">Polarizer defect (if Polarizer is used)</td><td>Scratch and damage on polarizer, particle on polarizer or between polarizer and glass.</td><td colspan="4">Refer to item 1 and item 2.</td></tr>
<tr><td rowspan="2">Bubble, dent and convex</td><td>A
B
C</td><td>Φ≦0.3
0.3<Φ≦0.5
0.5<Φ</td><td>Ignored
2
0</td><td rowspan="2">Ignored</td></tr>
<tr><td colspan="2">Total defective point(B,C)</td><td>2</td></tr>
</table>

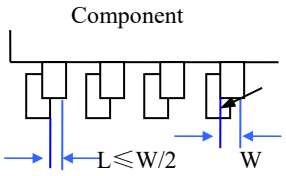
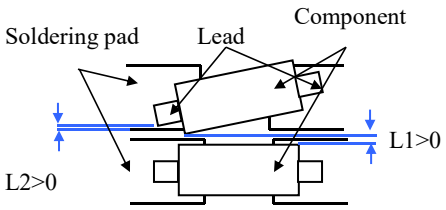
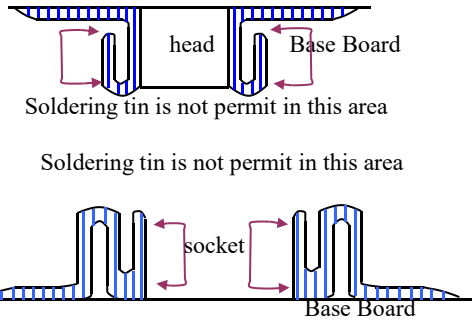
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Inspection item		Judgement standard		
		Category	Acceptable number	
			A zone	B zone
6	Surplus glass	①Stage surplus glass 	b≦0.3mm	
		②Surrounding surplus glass 	Should not influence outline dimension and assembling.	
7	MURA	①MURA	Naked eye examination: red, green, blue screen does not allow the appearance, black screen requires visual is not obvious, the specific reference limit samples. Note: the principle of closing the sample is to be installed on the whole machine and the end user will not find it in the normal usage scenario. Inspection basis: 6%ND (MURA mainly in the black screen and indoor light is relatively dark will be found, it is recommended to turn off the indoor lighting inspection.)	
		②Point Black / White / point(MURA)	1, under the black / gray screen check: D≦0.10mm Ignored; 0.10mm<D≦0.3mm, N≦2; D>0.3mm: Unqualified。 2, switch to the red, green, blue in which any one of the screen appears black or white or point to point white or point of failure. 	

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<table>
<tr><th colspan="3" rowspan="2">Inspection item</th><th colspan="2">Judgment standard</th></tr>
<tr><th colspan="2">Category(application: B zone)</th></tr>
<tr><td rowspan="5">8</td><td rowspan="5">Glass defect crack</td><td rowspan="4">①The front of lead terminals

</td><td>A</td><td>If a≦t and b ≦1.0,　c is not limited</td></tr>
<tr><td>B</td><td>a≦t, 1≦b≦2mm, c≦3mm</td></tr>
<tr><td>C</td><td>If glass crack cover alignment mark, b≦0.5mm.</td></tr>
<tr><td>D</td><td>Crack at two sids of lead terminals should not cover patterns and alignment mark</td></tr>
<tr><td>②Surrounding crack—non-contact side

</td><td colspan="2">b < Inner borderline of the seal</td></tr>
<tr><td>③ Surrounding crack— contact side

</td><td colspan="2">b < Outer borderline of the seal</td></tr>
<tr><td>④Corner

</td><td>A</td><td>a ≦ t, b ≦ 3.0, c ≦ 3.0
*Glass crack should not cover patterns used for</td></tr>
</table>

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Inspection item			Judgement standard
9	FPC defect	Component soldering: No cold soldering, short/open circuit, burr, tin ball. The flat encapsulation component position deviation must be less than 1/2 width of the pin (Pic.1)； The sheet component deviation: pin deviates from the pad and contact with the near components is not permitted（Pic.2）	
		lead defect: The lead lack must be less than 1/2of its width; The lead burr must be less than 1/2 of the seam; Impurities connect with the near leads is not permitted	
		Connector soldering: Soldering tin is at contact position of the plug and socket is not permitted No foundation is scald Serious cave distortion on plug and socket contact pin is not permitted	

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12.GENERAL PRECAUTIONS

1.1 HANDING

(1) When the module is assembled, it should be attached to the system firmly. Be careful not to twist and bent the module.

(2) Refrain from strong mechanical shock and / or any force to the module. In addition to damage, this may cause improper operation or damage to the module and back-light unit.

(3) Note that display modules are very fragile and could be easily damaged. Do not press or scratch the surface harder than a HB pencil lead.

(4) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, straining and discoloration may occur.

(5) If the display module surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, should be wiped by moisten cloth with isopropyl alcohol or ethyl alcohol solvents, DO NOT with water, ketone type materials (e.g. acetone), aromatic, toluene, ethyl acid or methyl chloride, and so on.

(6) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs or clothes, it must be washed away thoroughly with soap.

(7) Use finger-stalls with sort gloves in order to keep display clean during the incoming inspection and assembly process.

(8) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.

(9) Do not touch directly conductive parts such as the CMOS LSI pad and the interface terminals with bare hands, therefore operations should be grounded whenever he/she comes into contact with the modules.

(10) Do not exceed the absolute maximum rating value. (The supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on), otherwise the module may be damaged.

1.2 SOLDERING

(1) Use soldering irons with proper grounding and no leakage.

(2) For No RoHS Product: soldering temperature is 290~350°C, soldering time is 3~5s; for RoHS Product: soldering temperature is 340~370°C, soldering time is 3~5s.

(3) If soldering flux is used, be sure to remove any remaining flux after soldering (This does not apply in the case of a non-halogen type of flux).

1.3 STORAGE

(1) DO NOT leave the module in high temperature and high humidity for a long times, keep the temperature from 0°C to 35°C and relative humidity of less than 60%.

(2) It is highly recommended to store the module in a dark place. The Liquid crystal is deteriorated

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by ultraviolet, DO NOT leave it in direct sunlight and strong ultraviolet ray for many hours.

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